Evolutionary Algorithmical Approach for VLSI Floorplanning Problem

Hameem Shanavas .I and Gnanamurthy.R.K

Abstract—The Classical floor planning that usually handles only block packing to minimize silicon area, So modern floor planning could be formulated as a fixed-outline floor planning. It uses some algorithms such as B-TREE representation, simulated annealing and adaptive fast simulated annealing. Comparing above three algorithms the better efficient solution came from adaptive fast simulated annealing, its leads to faster and more stable convergence to the desired floorplan solutions. But the results are not an optimal solution. To get an optimal solution its necessary to choose effective algorithm. Combining global and local search is a strategy used by many hybrid optimization approaches. Memetic Algorithm (MA) is an evolutionary Algorithm that includes one or more local search phases within its evolutionary cycle. MA applies some sort of local search to improve the fitness of individuals in the population. The algorithm combines a hierarchical design technique, Genetic algorithms, constructive techniques and advanced local search to solve VLSI floor planning problem. MA quickly produces optimal or nearly optimal solutions for all the popular benchmark problems.

Index Terms—Floorplan Problem, Memetic algorithm, Genetic Algorithm, Delay, Cut size.

I. INTRODUCTION

Floorplanning is a critical step, as it sets up the ground work for a good layout. However, it is computationally quite hard. Very often the task of floor planning is done by a design engineer rather than a CAD tool. The process of determining block shapes and positions with area minimization objective and aspect ratio requirement is referred to as Floorplanning. A common strategy for blocks floorplanning is to determine in the first phase and then the relative location of the blocks to each other based on connection-cost criteria. In the second step, block sizing is performed with the goal of minimizing the overall chip area and the location of each block is finalized [2]. Simulated annealing (SA) has been considered a good tool for complex nonlinear optimization problems. The technique has been widely applied to a variety of problems.

However, a major disadvantage of the technique is that it is extremely slow and hence not suitable for complex

optimization problems such as scheduling. There are many attempts to develop parallel versions of the algorithm.

As an optimization technique, Genetic Algorithms simultaneously examine and manipulate a set of possible solutions. The power of GA's comes from the fact that the

Hameem Shanavas.I is the research Scholar of Anna University, Coimbatore, India.

Dr.R.K.Gnanamurthy is a Prof of Information and Communication Engineering, AnnaUniversity, Coimbatore, India

technique is robust, and can deal successfully with a wide range of problem areas, including those which are difficult for other methods to solve.

GA's are not guaranteed to find the global optimum solution to a problem, but they are generally good at finding "acceptably good" solutions to problems. Where specialized techniques exist for solving particular problems, they are likely to out-perform GA's in both speed and accuracy of the final result. Another drawback of Genetic Algorithms is that they are not well suited to perform finely tuned search, but on the other hand they are good at exploring the solution space since they search from a set of designs and not from a single design. The GA starts with several alternative solutions to the optimization problem, which are considered as individuals in a *population*. These solutions are coded as binary strings, called chromosomes. The initial population is constructed randomly. These individuals are evaluated, using the floorplanning-specific fitness function. The GA then uses these individuals to produce a new generation of hopefully better solutions. In each generation, two of the individuals are selected probabilistically as *parents*, with the selection probability proportional to their fitness. Crossover is performed on these individuals to generate two new individuals, called offspring, by exchanging parts of their structure. Thus each offspring inherits a combination of features from both parents. The next step is mutation. An incremental change is made to each member of the population, with a small probability. This ensures that the GA can explore new features that may not be in the population yet. It makes the entire search space reachable, despite the finite population size. A 3-point and 4-point crossover works best for our circuit floorplanning problem. In this implementation we have used the Roulette Wheel parent selection method which is conceptually the simplest stochastic selection technique. Our generation replacement technique is based on replacing the most inferior member in a population by new offsprings.

Genetic Algorithms are not well suited for fine-tuning structures which are close to optimal solutions. Incorporation of local improvement operators into the recombination step of a Genetic Algorithm is essential if a competitive Genetic Algorithm is desired. MAs are evolutionary algorithms (EAs) that apply a separate lo-cal search process to refine individuals (i.e) improve their fitness by hillclimbing. Under different contexts and situations, MAs are also known as hybrid EAs, genetic local searchers. Combining global and local search is a strategy used by many successful global optimization approaches, and MAs have in fact been recognized as a powerful algorithmic paradigm for evolutionary computing. In particular, the relative advantage of MAs over EAs is quite consistent on complex search spaces.



Manuscript received May, 2009; accepted June 19, 2009.

II. PROBLEM FORMULATION

Generally the floorplanning problems are such as size, Chip area, total wire length, delay of critical path, routability, noise, heat dissipation.

The modern floorplanning typically needs to pack blocks within a fixed die (outline) and additionally consider the packing with block positions as well as the interconnect constraints. The modern floorplanning problem is categories as Fixed-outline floorplanning.

A module B is a rectangle of height h_B , width w_B , and area A_B. A super-module consists of several modules, also called a sub-floorplan. A floorplan for n modules consists of an enveloping rectangle R subdivided by horizontal lines and vertical lines into n non-overlapping rectangles such that each rectangle must be large enough to accommodate the module assigned to it. In the given problem, we are given a set of hard modules and an outline-constraints are provided. The modules in the given Fixed-Outline (denoted as FO) have freedom to move while the modules outside the FO are infeasible in the final floorplan.A feasible packing is a packing in the first quadrant such that all the modules inside FO are not duplicate and overlapping. The objective is to construct a feasible floorplan R such that the total area of the floorplan R is minimized and simultaneously satisfy fixed-outline constraint. A slicing floorplan is represented by the slicing structure which can be obtained by recursively cutting a rectangle into two parts by either a vertical line or a horizontal line. As shown in Fig.2,a slicing floorplan can be represented as a slicing tree, that is, every leaf corresponds to a basic module and is marked by a number from 1 to n, and every internal node is labeled by a + or a *, corresponding to a horizontal or a vertical cut, respectively. Traversing the slicing tree in postorder, we obtain a Polish expression of length 2n - 1 for the slicing floorplan. A wheel is a nonslicing floorplan of five modules, which cannot be obtained by recursively cutting a rectangle into two parts by either a vertical line or a horizontal line Fig.3. Although slicing floorplans can be sub-optimal if compared to general floorplans, empirical evidence shows that slicing floorplans can be quite efficient in packing modules tightly. It has been proved mathematically and it is achieved for packing slicing floorplans tightly [1].





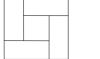


Fig 3 Non Slicing floorplan

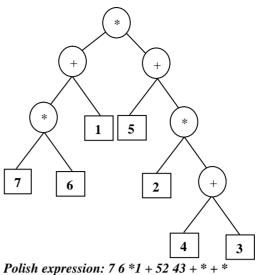


Fig 4. Slicing tree and corresponding polish expression.

III. MEMETIC ALGORITHM

Memetic Algorithms (MAs) are a class of stochastic global search heuristics in which EA-based approaches are combined with problem-specific solvers. The later might be implemented as local search heuristics techniques, approximation algorithms or, sometimes, even (partial) exact methods. The hybridization is meant to either accelerate the discovery of good solutions, for which evolution alone would take too long to discover, or to reach solutions that would otherwise be unreachable by evolution or a local method alone. As the large majority of MAs use heuristic local searches rather than, e.g., approximation or exact methods, in what follows we will focus only on the local search adding for the Evolutionary Algorithm (EA). It is assumed that the evolutionary search provides for a wide exploration of the search space while the local search can somehow zoom-in on the basis of attraction of promising solutions [6]. MAs have been proven very successful across a wide range of problem domains such as combinatorial optimization there are a number of benefits that can be gained by combining the global search of EAs with local search or other methods for improving and refining an individual's solution. However, as there are no free lunches these benefits must be balanced against an increase in the complexity in the design of the algorithm. That is, a careful consideration must be place on exactly how the hybridization will be done. The method is based on a population of agents and proved to be of practical success in a variety of problem domains and in particular for the approximate solution of Optimization problems. Consider for example the Memetic Algorithm template. A quick glance at the algorithm allows identifying the basic structure of an EA for which hot-spots, i.e. the places where hybridization could take place, have been identified and marked with red circles [6]. Each of these hot-spots provides opportunity for hybridization. For example, the initial population could be seeded with solutions arising from sophisticated problem specific heuristics, the crossover (mutation) operator could be enhanced with domain specific and representation specific constraints as to provide better search ability to the EA. Moreover, local search could be applied to any or all of the intermediate sets of solutions (e.g. the offspring set). However, the most popular form of hybridization is to apply one or more phases of local search, based on some probability parameter, to individual members of the population in each generation.

MEMETIC ALGORITHM

- 1. Encode Solution Space
- 2. (a) set pop size, max gen, gen=0;
- (b) set cross rate, mutate rate;
- Initialize Population.
 While(Gen < Gensize) Apply Generic GA Apply Local Search to Population EndWhile /* end of a run */
- 5.. Apply Final Local Search to Best Chromosome

A. Genetic Floorplan Algorithm

The overall procedure of GFA [5] is described in Fig.3.At the beginning, a set of Polish Expressions (PEs), denoted as P, is randomly generated to compose a population. The fitness of each sub-floorplan or floorplan, P, is calculated by eq .1

Fitness (p) =
$$\begin{array}{c} \mathbf{A}(\mathbf{R}) - \sum \mathbf{A} (\mathbf{B}_{i}) \\ Bi \, \varepsilon \, \mathbf{R} \qquad (1) \\ \underline{\mathbf{A}(\mathbf{R})} \end{array}$$

GENETIC ALGORITHM

- 1. Encode Solution Space
- 2. (a) set pop size, max gen, gen=0;(b) set cross rate, mutate rate;
- 3. Initialize Population. 4. While max gen ,gen **Evaluate Fitness** For (i=1 to pop size) Select (mate1, mate2) if (rnd(0.1) < cross rate)child = Crossover(mate1,mate2); if (rnd(0.1))< mutate rate) child = Mutation(); Repair child if necessary End For Add offsprings to New Generation. gen = gen + 1End While Return best chromosomes.

where Block Area is the area of each module and Floorplan Area is the area of the floorplan, consisted of modules. The fitness calculates the proportion of the dead area of a floorplan, that is, the more the fitness closes to zero. The threshold value T_s , is initially set closes to zero, which is used to identify the quality of sub-floorplans or a floorplan. If a sub-floorplan satisfies equation (2), it is identified as a good sub-floorplan, denoted as g_s .

Fitness
$$(\mathbf{p}_i) \ll \mathbf{T}_s$$
 (2)

The T_s will be increased with a little value D when the best solution is not improved after a period of time. Two major evolutional operations, crossover_FO and mutation, used in the algorithm are explained in more details below. They are respectively executed cr*popsize and mr*popsize times, where cr and mr are crossover rate and mutation rate and popsize is the number of Polish expressions in the population. The whole algorithm does not stop until the best solution is not improved after a given number of generations.

B. Crossover_FO

The process of the crossover operation to solve the constraint of Fixed-Outline is explained. The input is a pair of floorplan P_1 and P_2 . The output is a floorplan that inherits good sub-floorplans from P and P and satisfies the fixed-outline constraint. Assume the number of gs of P_1 is larger than or equal to the number of gs of P_2 . There are four steps in this operation [6].

Step 1: Extract all gs from P_1 subject to the fixed-outline constraint. That is, the width and height of good sub-floorplans in gs_1 is less than or equal to width and height of the given fixed-outline, respectively.

Step 2: Extract all gs from P₂ subject to the fixed-outline constraint, and no modules in gs appeared in gs₂.After that,gs₁ and gs₂ are put together into a Good Sub-floorplan Pool, GsP, i.e. $GsP = gs_1 U gs_2$. The set of modules that are not appeared in GsP are viewed as degenerated good sub-floorplan,denoted as dgs. Put dgs into GsP, i.e. GsP = GsP U dgs.

Step 3: Enlarge good sub-floorplans in GsP, and the process of mergence is performed. The mergence randomly checks vertical relation, i.e. +, and horizontal one, i.e.*.The mergence of the selected pair of gs will be performed if the fitness satisfies equation (2) and the width/height satisfies the fixed-outline constraint. Remove the two merged gs and put the newly generated gs into GsP. Note that this step repeats until it can not find any gs that satisfies the fixed-outline constraint to avoid the occurrence of infinite loop. This step is continued until there exists only one gs in GsP, i.e. A complete floorplan is generated.

Step 4: When a new floorplan is generated, evaluate it by equation (1). If the fitness of the new floorplan is less than or equal to one of ancestors, the new floorplan will be selected to replace the worst floorplan of ancestors.

C. Mutation

There are three possible operations for mutation. These operations are randomly selected to perform mutation in order to escape the space of local optimal search. These operations are as follows [5].

Op1: complement a relational operator to another one of a P, i.e. * to + or + to *.

Op2: exchange two modules, a subfloorplan and a module, or two sub-floorplan of a P.

Op3: rotate a module or a sub-floorplan of a P.

D.Local search:

Simulated annealing (SA) [11] is widely used for floorplanning. It is an optimization scheme with non-zero probability for accepting inferior (uphill) solutions. The probability depends on the difference of the solution quality and the temperature. The probability is typically defined by min {1,exp- $\Delta C/T$ },where ΔC is the difference of the cost of the neighboring state and that of the current state ,and T Is the current temperature. In the classical annealing schedule, the temperature is reduced by a fixed ratio for each iteration of annealing.



IV. EXPERIMENTAL RESULT

To test the effectiveness of proposd Fixed-outline floorplanning algorithm, set the maximum percentages of dead space to 15% and 10%. The expected aspect ratios R * of the floorplans are chosen from the range with interval 0.5. Experiments were performed on a 1.6-GHz Intel Pentium4 PC using the GSRC benchmark circuit n100.The results were averaged for 50 runs for each aspect ratio. We compared with FASA based on the same platform. We have tested MA with polish expression floorplan representations, polish Table I lists the average success rates for FASA and the MA. Proposed method obtained 100% success rates of fitting into the given fixed outlines for all runs with dead space $\Gamma = 15\%$ and $\Gamma = 10\%$. Incontrast, the success rates when $\Gamma = 10\%$ for MA, FASA were 30.3%, 65.5%, and 99.4% respectively. The dramatic differences reveal the effectiveness of our approach. Also, this proposed method required the least running time on average.



	Asp	Fast-SA		ours	
	ect	Wire	Time	Wire	Time
Circ	Rat	(mm)	(sec)	(mm)	(sec)
uit	io				
	R*				
n100	1	33.56	30	32.06	26
	2	35.44	30	34.39	24
	3	35.48	30	34.23	27
	4	36.89	29	32.74	27
n200	1	63.55	175	58.33	150
	2	62.76	173	59.84	156
	3	63.70	180	61.55	156
	4	66.31	176	63.72	171
n300	1	76.05	399	71.00	363
	2	77.60	386	74.22	358
	3	81.67	391	79.56	371
	4	88.58	382	82.18	370
Comparison		1.06	1.11	1.00	1.0

V. CONCLUSION

The proposed algorithms for modern Floorplanning problems with Fixed-outline is based on the new Memetic algorithm. Experimental results have shown that MA leads to faster and stable convergence to desired Floorplan solutions. For fixed-outline floorplanning, the new cost function considering the aspect-ratio penalty drives MA more efficiently to find floorplans inside the given chip outline. The experimental results on the fixed-outline floorplanning have shown the efficiency and effectiveness of our Floorplanning algorithms; for those applications, our results out perform the related recent Works by large margins. Research along this direction is on going.

ACKNOWLEDGEMENT

It is to note here that this topic-specific article for the easy reference of VLSI Floorplanning Problem, optionally being used together with the all the problems in VLSI Physical Design. Author Hameem Shanavas.I thank the management of Vivekanandha College of Engineering for Women,India to conduct his research in this arena and also he worked under the Guidance of Dr.R.K.Gnanamurthy, the Eminent Professor in India. He thank the guide for his moral support to continue the research.

REFERENCES

- [1] .S.Kirpatrick, C.D. Gelatt, and M.P.Vecchi, "Optimization By simulated annealing", Science, pp.671-680, 1983.
- [2] .N. Sherwani, Algorithms for VLSI Physical Design Automation, Kluwer Academic Publishers, Boston, 1999
- [3] Y.C. Chang, Y.W. Chang, G.M. Wu, and S.-W.Wu, "B* -trees: A new representation for non-slicing floorplans," in Proc.ACM/IEEE Design Automation Conf., Los Angles, A, Jun 2000, pp 458-463
- [4] S. N. Adya and I. L. Markov, "Fixed- outline floorplanning through better local search," in Proc. IEEE Int. Conf. Computer Design, Austin, TX, 2001, pp. 328–334.
- [5] Chang-Tzu Lin, De Sheng Chen, Yiwen.Wang, "An Efficient Genetic Algorithm for Slicing Floorplan Area Optimization", Proceedings of the International Symposium on Circuits And Systems, pp. 879-882, 2002.
- [6] Changq Tzu Lin, De Sheng Chen, Yiwen Wangs, Hsin-Hsien Ho "Modern Floorplanning with Abutment and Fixed-Outline Constraints", Proceedings of the International Symposium on Circuits and Systems, pp. 879-882, 2002.
- [7] Stephen Coe, Shawki Areibi, Medhat Moussa "A Hardware Memetic Accelerator for VLSI Circuit Partitioning" University of Guelph, School of Engineering, Guelph, Canada, 2003.
- [8] T.C.Chen and Y.W.Chang, "Modern floor planning based on fast simulated annealing", in Procz ACM Int Symp Physical Design San Francisco, CA, Apr.2005, and pp 104–112.
- [9] .Natalio Krasnogor and Jim Smith, "A Tutorial for Competent Memetic Algorithms: Model, Taxonomy and Design Issues", IEEE transactionson evolutionary computation, vol.a, no.b, ccc200d, March 2005.
- [10] Natalio Krasnogor, Alberto Aragón and Joaquin Pacheco. "MEMETIC ALGORITHMS", School of Computer Science and I.T. University of Nottingham. England 2005.
- [11] Tung-Chieh Chen, Student Member IEEE, and Yao Wen Chang, "Modern floor planning based on B-tree and fast simulated annealing" Member IEEE transactions on computer-aided design of integrated circuits and systems, vol.25, April 2006.
- [12] Maolin Tang, Member IEEE, and XinYao, Fellow IEEE "A Memetic Algorithm for VLSI Floor planning". IEEE transactions on systems, man, and cyber net, vol.37, no.1, february2007.
- [13] The MCNC Benchmark Problems for VLSI Floorplanning. [Online]. Available: <u>http://www.mcnc.org</u>

Hameem Shanavas.I is the research Scholar of Anna University, Coimbatore, India. He has completed his Bachelor Degree in Electronics and Communication(2006), Masters in VLSI Design(2008) and also he completed Masters in Business Administration(2009).He has published many journals and attended many Conferences in National and International Level. His research area is VLSI Physical Design. email:hameemshan@gmail.com. phone:00919894594647.

Dr.R.K.Gnanamurthy, Head of Vivekanandha College of Engineering for Women. He has completed his PhD from Anna University ,Chennai.He has published more than 20 journals in International Level.He is the Chief Coordinator for many Government Projects.His reaerch areas are Mobile Computing,Networking.