An Iteration Obfuscation Based on Instruction Fragment Diversification and Control Flow Randomization

Xin Xie, Fenlin Liu, Bin Lu, and Fei Xiang

Abstract—As the control flow graph can reflect the logic structure of programs, static and dynamic reverse methods are used to analyze the logic structure and instruction sequence, and the existing methods of control flow obfuscation have low potency to resist reverse attacks. To solve this problem, we propose an obfuscation method based on instruction fragment diversification and control flow randomization. These instruction fragments are generated by equivalent transformation rules, and random functions are used to select one execution path from the multi-way branches of programs, then programs are iteratively obfuscated. Experiments and analysis show that diversified instruction fragments and multi-way branches can increase the difficulty of static reverse analysis, random selection for multi-way branches will increase the difficulty of dynamic instruction tracing, and iterative transformation for many times enhances the complexity of control flow graph.

Index Terms—Code obfuscation, iterative transformation, instruction fragment diversification, control flow randomization.

I. INTRODUCTION

Malicious attackers are always locating, analyzing and extracting important codes in programs by static and dynamic reverse analysis. Through malicious modification, core modules can be migrated to other programs which can be sold as new software products. This process seriously violates software intellectual property [1]. In order to protect software code, many technologies have been proposed, such as code obfuscation [2], software watermarking [3], software tamper-proofing [4] and software birthmarking [5]. They can be used to prevent software from reverse analysis, illegal distribution, copying and malicious tampering.

Code obfuscation is a typical software protection technology. On the basis of keeping program function, code are semantics-preserving transformed to be more difficult for attackers to reverse analyze and understand. The existing obfuscation techniques can be classified into four categories, including control obfuscation [6], data obfuscation [7], layout obfuscation, and preventive obfuscation [8]. Control obfuscation can make program logic complicated by modifying or hiding the real control flow information, the main methods are opaque predicates inserting, control structure flattening, and control flow hiding. Obfuscation algorithms based on opaque predicates add true, false and uncertain condition branch states into programs, thus it can make control flow more complicated. Obfuscation algorithms based on control flow flattening increase the number of basic blocks and control flow edges, and they can increase the complexity of control flow graph. Obfuscation algorithms based on control flow hiding transform control flow into data or exception information.

Existing methods of control flow obfuscation have increased complexity and stealthiness of control flow to a certain extent, and they can also resist static reverse analysis. However, both static and dynamic methods are used to analyze control flows. Control flow can also be analyzed by tracking instruction execution traces. Therefore the potency of existing control flow obfuscation should be improved for the goal of resisting static and dynamic reverse analysis. To solve this problem, iteration obfuscation based on instruction fragment diversification and control flow randomization is proposed. To split basic blocks, diversified instruction fragments are generated by equivalent transformation rules, and multi-way branches are constructed. These instruction fragments can be executed in some branches. Random functions are used to select different branches, and instruction fragments in basic blocks can be iteratively obfuscated.

II. RELATED WORKS

The first obfuscation was proposed by Diffie and Hellman [9] when they did not use the obfuscation word to describe the technique. Collberg et al. introduced the technique to protect Java code program. In recent years, many researchers have proposed various obfuscation methods that can mainly fall into five types, including data obfuscation, control flow obfuscation, layout obfuscation, preventive obfuscation and some other advanced obfuscation.

Data obfuscation is described as breaking abstractions and data structures. Collberg et al. suggested restructuring program arrays by splitting, merging, folding and flattening [8]. They described array obfuscation by simple examples, but did not show detailed algorithm. Zhu et al. applied homomorphic functions to improve the potency of program arrays obfuscation [10]. They made program arrays more obscure to be understood, but did not give the formal description of data obfuscation. Drape et al. regarded obfuscation as data refinement to prove the correctness of obfuscation and generalize array splitting obfuscation [11],...
then they established a framework of the above theory [12]. Xin et al. designed an automatic data structure obfuscation technique as a GNU GCC compiler extension to implement field reordering [13], and more specific approaches, such as garbage field inserting and structure splitting, are under consideration.

Control flow obfuscation obstructs the control flow information of programs. Collberg et al. constructed opaque predicates based on numerical theory and pointer aliasing technology, and the branch states in program were modified by opaque predicates [14]. Yang et al. proposed the multi-point function opaque predicate obfuscation algorithm, which could make opaque predicates interdependent and avoid reverse slicing attacks [15]. Wang et al. transformed direct control jumps into indirect control jumps by pointer aliasing, and implemented structure flattening of control flow in procedure [16]. Considering the difficulty of intra-procedure calling analysis, Ogiso et al. transformed control structure of intra-procedure by function pointers [17]. Toyofuku et al. proposed to use random values to choose method calls in programs that could complicate method call graphs [18]. Popov et al. introduced exception handling mechanism to modify and replace control flow of programs that could hide the branch information in control flow [19]. Balachandran and Emmanuel proposed control obfuscation based on stack self-modified code, and the method removed control flow information from code section and hid them in data section [20].

Layout obfuscation obscures layouts of programs on the intermediate language code or source code. Chan et al. suggested overusing an identifier to improve Java layout obfuscation based on substituting the identifiers with meaningless names [21] Xu et al. applied whitespace randomization, comments randomization, and variable and function names randomization to JavaScript [22], these methods were all for source codetransformation.

Preventive obfuscation makes specific deobfuscation techniques more difficult to succeed. Lim et al. designed an obfuscation to thwart linear sweep and recursive traversal static disassembly algorithms [23], which introduced junk instructions, jump tables, and opaque predicates to disrupt the static disassembly process. Ledoux et al. proposed an instruction embedding method to improve instruction overlapping obfuscation [24], Batchelder et al. presented layout obfuscation algorithm to make Java decompilers fail to produce legal source code or crash completely [25].

Advanced obfuscation transforms programs at run-time or deploys important code on the remote trusted entity. Anckaert et al. suggested a compromise between distributing identical copies and unique executions by diversifying at run-time [26], which made it harder to zoom in on a point of interest and may fool an attacker into believing that he had succeeded. Abadi et al. presented layout randomization for obfuscation and studied it in programming-language terms [27]. Roeder et al. designed proactive obfuscation that periodically restarting servers with these diverse versions [28], and the periodic restarts helped to bound the number of compromised replicas that a service ever concurrently ran to make an adversary’s job harder. Wang et al. proposed a branch obfuscation approach that replaced explicit conditional jump instructions with implicit trap code and bogus code, and deployed jump conditions on the remote trusted entity [29].

III. ITERATION OBfuscATION BASED ON INSTRUCTION FRAGMENT DIVERSIFICATION AND CONTROL FLOW RANDOMIZATION

A. Equivalent Transformation Rules

In order to implement instruction fragment diversification, some equivalent transformation rules are proposed such as instruction position transformation, register transformation, junk instruction insertion, equivalent instruction replacement, and instruction expansion transformation [30].

1) Instruction position transformation rule

When we exchange the positions of instructions which have no dependence, it will not influence the execution of programs, thus diversified instruction fragments can be generated. Let there be no dependence in \( n \) instructions \((a_1, a_2, \ldots, a_n)\), where instructions can be exchanged arbitrarily, therefore \( n! \) different kinds of instruction sequences can be got. To analyze dependence between instructions in the sequence, position transformation rules are listed as follows:

1) If instructions define and use the same common register, their positions cannot be exchanged.
2) If instructions impact on the same flag register, their positions cannot be exchanged.
3) If instructions define and use the same memory, their positions cannot be exchanged.
4) If instructions push parameters and then call functions or APIs, their positions cannot be exchanged.

Example code fragment extracted from program is shown in Table I. By instruction position transformation rule, there is no control and data dependence among these instructions, and they can be arrayed as \( 6 \) different kinds of sequences.

<table>
<thead>
<tr>
<th>TABLE I: POSITION TRANSFORMATION OF EXAMPLE CODE FRAGMENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(1) mov eax, 1</td>
</tr>
<tr>
<td>(2) xor ebx,ebx</td>
</tr>
<tr>
<td>(3) lodsd</td>
</tr>
<tr>
<td>---------------------------------------------------------</td>
</tr>
<tr>
<td>(1) lodsd</td>
</tr>
<tr>
<td>(2) xor ebx,ebx</td>
</tr>
<tr>
<td>(3) mov eax,1</td>
</tr>
<tr>
<td>---------------------------------------------------------</td>
</tr>
<tr>
<td>(1) xor ebx, ebx</td>
</tr>
<tr>
<td>(2) mov eax,1</td>
</tr>
<tr>
<td>(3) xor ebx,ebx</td>
</tr>
</tbody>
</table>

2) Register transformation rule

On the basis of not affecting program normal execution, some registers in code fragments can be replaced by other registers, such as \( \text{mov reg, 0x1234} \), \( \text{mov [0x5678]} \), \( \text{reg} \), where \( \text{reg} \) can be the register that is randomly selected from \( eax \), \( ebx \), \( ecx \), \( edx \) and so on. Register transformation rule can also be shown as follows: firstly values of needed registers are saved at the beginning of code fragments, and then these registers are used to replace other different registers, finally values of needed registers will be restored after the process of replacing, such as \( \text{and reg1, reg2} \), \( \text{sub reg2, reg1} \) can be transformed into \( \text{push reg3, push reg4, and reg3, reg4, sub reg4, reg3, mov reg1, reg3, mov reg2, reg4, pop reg4, pop reg3} \).

3) Junk instruction insertion rule

There are many forms of junk instructions that will not affect execution of instruction sequence in program. Some
junk instructions are as follows:
1) Nop instruction.
2) Add reg, value; sub reg, -value; push reg; pop reg.
3) Jmp offset address, push eax, push ebx, call API (junk instructions), @mov eax, ebx, add eax, ebx (useful instructions).
4) mov eax, value1, add eax, value2, sub eax, value3 (junk instructions), mov eax, reg1 (useful instructions).
5) Other junk instruction sequences.

4) Equivalent instruction replacement rule

Because an instruction can be replaced by another equivalent instruction, one code fragment is constructed by instruction sequence \( I_1, I_2, \ldots, I_n \), where instruction \( I_i \) has the same function as \( I_{p1}, I_{p2}, \ldots, I_{pm} \). Some replacement rules of equivalent instruction are shown in Table II.

### TABLE II: SOME REPLACEMENT RULES OF EQUIVALENT INSTRUCTION

<table>
<thead>
<tr>
<th>Original Instruction</th>
<th>Equivalent Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sub&lt;register/memory&gt;,&lt;value&gt;</td>
<td>Add&lt;register&gt;,&lt;value&gt;</td>
</tr>
<tr>
<td>Not&lt;register/memory&gt;</td>
<td>Xor&lt;register/memory&gt;,.-1</td>
</tr>
<tr>
<td>Nop</td>
<td>And&lt;register/memory&gt;,.-1</td>
</tr>
<tr>
<td>Sub&lt;register/memory&gt;,&lt;value&gt;</td>
<td>Lea&lt;register&gt;,</td>
</tr>
<tr>
<td>Mov&lt;register a&gt;,&lt;register b&gt;</td>
<td>Lea&lt;register a&gt;,DWORD&lt;value&gt;</td>
</tr>
<tr>
<td>Add&lt;register/memory&gt;,&lt;value&gt;</td>
<td>Sub&lt;register/memory&gt;,&lt;value&gt;</td>
</tr>
<tr>
<td>Inc&lt;register/memory&gt;</td>
<td>Add&lt;register/memory&gt;,&lt;value&gt;</td>
</tr>
<tr>
<td>Lea&lt;register&gt;/&lt;register&gt;</td>
<td>Add&lt;register/memory&gt;,&lt;value&gt;</td>
</tr>
</tbody>
</table>

5) Instruction expansion transformation rule

### TABLE III: SOME EXPANSION TRANSFORMATION RULES OF EQUIVALENT INSTRUCTIONS

<table>
<thead>
<tr>
<th>Original Instruction</th>
<th>Equivalent Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>Push&lt;value/register&gt;</td>
<td>Mov DWORD PTR SS:[ESP-4],&lt;value/register&gt; Sub esp,4</td>
</tr>
<tr>
<td>Mov&lt;register a&gt;,&lt;register b&gt;</td>
<td>Push&lt;register a&gt; Pop&lt;register b&gt;</td>
</tr>
<tr>
<td>Jmp&lt;address&gt;</td>
<td>Push&lt;address&gt; Retn</td>
</tr>
<tr>
<td>Pop&lt;register/memory&gt;</td>
<td>Mov&lt;register&gt;,DWORD[esp] Add Esp,4</td>
</tr>
<tr>
<td>Call&lt;address&gt;</td>
<td>Push EIP+bytes to next instruction Jmp&lt;address&gt;</td>
</tr>
<tr>
<td>Mov&lt;register a&gt;,&lt;register b&gt;</td>
<td>Mov&lt;value/memory&gt;,&lt;register a&gt; Mov&lt;register b&gt;,&lt;value/memory&gt;</td>
</tr>
<tr>
<td>Not&lt;register/memory&gt;</td>
<td>Neg&lt;register/memory&gt; Sub&lt;register/memory&gt;,1</td>
</tr>
<tr>
<td>Push&lt;value/register&gt;</td>
<td>Mov&lt;register/memory&gt;,&lt;value/register&gt;</td>
</tr>
<tr>
<td>Retn</td>
<td>Pop eax Jmp eax</td>
</tr>
<tr>
<td>Pop&lt;register/memory&gt;</td>
<td>Mov&lt;value/register&gt;</td>
</tr>
<tr>
<td>Call&lt;address&gt;</td>
<td>Mov&lt;register&gt;,&lt;value/register&gt; Call&lt;address&gt;</td>
</tr>
<tr>
<td>Neg&lt;register/memory&gt;</td>
<td>Not&lt;register/memory&gt; Add&lt;register/memory&gt;,1</td>
</tr>
</tbody>
</table>

Instruction expansion transformation refers to replace one instruction with instruction sequence of the same semantics. Code fragment is constructed by instruction sequence \( I_1, I_2, \ldots, I_n \), where instruction \( I_i \) has the same function as \( I_{p1}, \ldots, I_{pm} \). This instruction sequence \( I_{p1}, I_{p2}, \ldots, I_{pm} \) is a transformation rule of equivalent expansion for instruction \( I_i \).

B. Instruction Sequence Diversification

Generally, single instruction sequence has a variety of semantics-preserving expression forms, such as instruction sequence \( S=(o_1, o_2, \ldots, o_n) \), where \( o \) denotes an instruction, instruction sequence \( S \) can be transformed into many sequences \( S_1, S_2, \ldots, S_n \) by equivalent transformation rules. If equivalent instruction sequences \( S_1, S_2, \ldots, S_n \) are different from \( S \), but they implement the same function as \( S \), they are the diversified sequences of \( S \).

### Algorithm1 Diversified Instruction Fragments Generation Algorithm

Input: Instruction Fragment \( S=(o_1, o_2, \ldots, o_n) \), Iteration Number \( m \)
Output: Set of Diversified Instruction Fragments \( VS \)

1. \( S=(o_1, o_2, \ldots, o_n) \); \( k=1, d=0 \)
2. while \( k<=m \) do
3. \( INS=\{ok\} \)
4. \( INS\leftarrow\{ins\} \)
5. while \( d<m \) do
6. \( ins\leftarrow\text{random}(INS)\); \( \text{Choose any ins in INS} \)
7. \( T=\text{Transform}(ins)\); \( \text{Expansion Transform of chosen instruction} \)
8. if \( T=\text{NULL} \) then
9. \( \text{INStmp= } T \)
10. \( \text{INS= INS+INStmp- } \) ins;
11. \( \text{ins= ins-INSStmp- } \) ins;
12. \( \text{VS=VS} \)
13. 
14. 
15. 
16. \( k++ \)
17. end while
18. return VS

In order to increase the number of diversified sequences, instruction fragments in programs are iteratively obfuscated with instruction expansion transformation rules [31], as a result, the number of instruction sequences will be exponentially increased. Diversified instruction fragments generation algorithm is shown in Algorithm 1. Because there are many equivalent transformation rules, the composite transformation rules can be used to generate diversified instruction fragments.

C. Control Flow Randomization

Equivalent instruction fragments are the sequences in program execution paths, and multi-way branches are randomly constructed in programs. When a branch state appears in a program, a random function is used to select different branches. Due to the random selection of branches, the execution path is different each time.

Considering the generation speed and randomness, Microsoft security cookie mechanism is used for reference. The value of \( m_i \) can be obtained by calling system APIs during the execution of program, random value \( Vr \) calculated by formula (1) is used to be the seed of function \( srand() \), where
$m_1$ (GetSystemTimeAsFileTime) is the current system time; $m_2$ (GetCurrentProcessId) is the current process ID; $m_3$ (GetCurrentThreadId) is the current execution thread ID; $m_4$ (GetTickCount) is the millisecond of the current operating system from boot to now; $m_5$ (QueryPerformanceCounter) is the CPU clock frequency.

\[
V_r = \text{Hash}(m_1 \oplus m_2 \oplus m_3 \ldots \oplus m_5) \quad (1)
\]

Code snippets of control flow randomization uses random value of $V_r$ as a seed of random number generator, function rand() is used to generate pseudo random number, and the generated value is used to randomly select multi-way branches in the program, it is shown in Table IV, where $H_0$-$H_5$ have the same function.

<table>
<thead>
<tr>
<th>TABLE IV: CODE SNIPPETS OF CONTROL FLOW RANDOMIZATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1: $V_r = \text{Hash}(m_1 \oplus m_2 \oplus m_3 \ldots \oplus m_5)$</td>
</tr>
<tr>
<td>2: srand($V_r$);</td>
</tr>
<tr>
<td>3: $N = \text{rand}() % 4$;</td>
</tr>
<tr>
<td>4: switch($N$)</td>
</tr>
<tr>
<td>5: {</td>
</tr>
<tr>
<td>6: case 0: $H_0$; break;</td>
</tr>
<tr>
<td>7: case 1: $H_1$; break;</td>
</tr>
<tr>
<td>8: case 2: $H_2$; break;</td>
</tr>
<tr>
<td>9: case 3: $H_3$; break;</td>
</tr>
<tr>
<td>10: }</td>
</tr>
</tbody>
</table>

D. Obfuscation Step

**Step 1:** All head instructions are labeled including first instruction in function, target instruction of transfer instruction, instruction followed by transfer instruction. Basic blocks are divided according to head instructions, and then control flow graph $G=(V, E)$ is built.

**Step 2:** The basic blocks in control flow graph are chosen if the number of instructions are greater than 2, then they can be applied with diversified transformation rules.

**Step 3:** A single basic block will be divided into three parts: the first and last instruction in basic block, and the instruction sequence between them, denoted by $B_0 = \{I_{start}, I_{end}, \text{Seq}\}$.

**Step 4:** The instruction sequence between the first and last instruction in basic block will be divided into two parts, denoted by $\text{Seq} = \{B_{seq1}, B_{seq2}\}$. The set of diversified instruction fragments are constructed with transformation rules, such as instruction position transformation, register transformation, junk instruction insertion, equivalent instruction replacement, and instruction expansion transformation, the instruction fragment set can be denoted by $\{B_{seq1}, B_{seq2}, \ldots B_{seqn}\}, \{B_{1seq}, B_{2seq}, B_{3seq}, \ldots B_{mseq}\}$.

**Step 5:** Two layers of multi-way branches are built based on the set of diversified instruction fragments, such as $E = \{(B_{seq1}, B'_{seq1}), (B_{seq2}, B'_{seq2}), (B_{seq3}, B'_{seq3}), (B_{seq4}, B'_{seq4}), \ldots (B_{seqn}, B'_{seqn}), (B_{1seq}, B'_{1seq}), (B_{2seq}, B'_{2seq}), (B_{3seq}, B'_{3seq}), \ldots (B_{mseq}, B'_{mseq})\}$.

**Step 6:** Randomization of multi-way branches in a basic block is implemented by using random function. The basic block semantics will be not changed.

**Step 7:** After reiteratively executing step 3-6 for instruction fragments, the basic block will be more complicated.

**Step 8:** All basic blocks are iteratively obfuscated with diversified splitting rule.

Through the above steps, two basic blocks in Fig. 1(a) will be iteratively obfuscated. Control flow graph will be transformed into Fig. 1(b) with the iterative obfuscation rule first time. If the iterative obfuscation rule is used once again on the gray basic blocks in Fig. 1(b), control flow graph will be transformed into Fig. 1(c). There is no doubt that the number of basic blocks and control flow edges will increase greatly after many times of obfuscation, and the control flow graph will be more complicated, thus it can signally increase the difficulty of static and dynamic reverse analysis.
E. A Case in Study

If an instruction fragment in a basic block can be divided into the execution modules such as H1, H2, H3, and H4, the code snippets and control flow graph are shown in Fig. 2. Obfuscated program is constructed by instruction fragment diversification and control flow randomization, as shown in Fig. 3, where instruction fragments (31, 32, 33, 34) and (41, 42, 43, 44) are generated from the instruction fragments 3 and 4 with diversified transformation rules. The original basic block only has one execute path, such as 1->2->3->4. Fig. 3 reflects that the number of execution paths in obfuscated program has increased to 10, including 1->2->31->41, 1->2->33->42 and so on. Obfuscation has greatly increased the complexity of control flow graph and the difficulty of static and dynamic reverse analysis.

IV. METRIC ANALYSIS

A. Potency Measurement

Potency includes the performance of obfuscation to resist static and dynamic reverse analysis. Static reverse analysis is used to analyze the nodes and edges of control flow graph nodes in control flow graph are instruction fragments generated with diversified transformation rules, and edges are execution paths that can be selected by random functions. Dynamic reverse analysis is used to analyze instruction traces.

Let the length of basic block \( S=(o_1,o_2,\ldots,o_n) \) be \( n \). The basic block is divided into two parts by reserving the first instruction \( o_1 \) and the last instruction \( o_n \), and the basic block can be transformed with equivalent transformation rules. Let the length of \( o_2,\ldots,o_n \) be \( l-1 \). Each instruction in the sequence will be extended to \( k \) instructions, and \( C_2^1+C_2^1+\ldots+C_2^k \) kinds of instruction fragments will be generated after first time obfuscation. And \( C_2^1(2^2-1)+C_2^1(2^{2^2}-1)+\ldots+C_2^1(2^{2^k-1}) \) kinds will be generated after second time. On the basis of these fragments, the number of instruction fragments will increase after implementing composite transformation rules. Let instruction fragments \( (o_1,\ldots,o_l) \) and \( (o_{l+1},\ldots,o_{n+1}) \) respectively generate \( k \) and \( d \) kinds of instruction fragments with diversified transform rules, and they are used to be the instruction execution sequences in multi-way branches. If \( k>d \), the minimum number of branch paths is \( k \), and the maximum number of branch paths is \( kd \).

Let the control flow graph of the original program be \( G=(V,E) \), where the value of \( V \) is \( n \), and value of \( E \) is \( e \), so the complexity of graph is \( e = v + e + 2 \) by cyclomatic complexity measurement [32]. Let the obfuscated instruction fragments be \( m \) kinds, and they are generated with equivalent transformation rules, so there are \( m^m \) branch paths between instruction fragments. The Number of nodes will be \( v(2m^2+2) \) after first time obfuscation, the number of control flow edges will be \( e + v(m^m+2m) \), and the cyclomatic complexity of control flow graph will be \( e + v(m^m+2m) - (2m^2+2) \). After \( k \)th time iterative obfuscation, the nodes of control flow graph will be \( v(2k^2m^{m+k}) \), the edges of control flow graph will be \( e + v((2m^m+m^m)\sum_{k=0}^{m}(m^k)^{m+k}) \), and the cyclomatic complexity of control flow graph will be \( e + v((2m^m+m^m)\sum_{k=0}^{m}(m^k)^{m+k}) \).

B. Cost Measurement

Cost measurement mainly considers the execution time and file size of original and obfuscated programs. Let \( u \) instructions be executed \( k \) times in the condition of looping, if one instruction will cost a unit of storage space and execution time, the cost of space and time will be \( u \) and \( uk \) respectively. Instruction fragments are generated by transformation rules, and let \( m \) kinds of instruction fragments be generated after first time obfuscation, and obfuscation will be applied to the instruction fragments for \( n \) times. If the length of one instruction fragment is \( s_k \), the time cost of obfuscated program is \( k(2^s_k+\sum_{k=0}^{m}(m^k)^{m+k}) \), and the space cost of obfuscated program is \( \sum_{k=0}^{n}(2^s_k+\sum_{k=0}^{m}(m^k)^{m+k}) \).

As the obfuscation time increased, the structure of control flow graph will be greatly complicated, and the file size of program will increase, and the number of instructions will be expanded. When we use the iterative obfuscation to transform programs, the time of iteration need to be controlled for the trade-off among space cost, time cost and potency.

C. Deobfuscation Measurement

The existing deobfuscation algorithms are based on the optimization theory [33], including peephole optimization, constant propagation, constant folding, operand folding, and stack optimization. These methods can remove junk instructions and invalid branches in functions that deobfuscate redundant codes to a certain extent.

Diversified instruction fragments are generated with junk instructions insertion, register replacement and some other transformations, therefore deobfuscation algorithms can optimize some instruction fragments. Equivalent instruction fragments in multi-way branches are generated by equivalent rules of instruction replacement and instruction expansion that present diversity. If these fragments will be normalized, the semantics of different instruction fragments will be analyzed, and it is very difficult to deobfuscate the obfuscation.

D. Stealth Measurement

Stealth measures the difficulty of recognizing whether obfuscation or not, the similarity of original and obfuscated programs can be the stealth metric of quantitative analysis. Similarity of programs can be calculated with sequence, set and graph formula [34].

To measure the stealth of obfuscation, similarities of original and obfuscated programs are calculated, similarity formula based on maximum common subgraph is used. Let \( G \), \( G_1 \) and \( G_2 \) be graphs, if a subgraph in \( G_1 \) is isomorphism as \( G \), and a subgraph in \( G_2 \) is isomorphism as \( G \), so that \( G \) is the common graph between \( G_1 \) and \( G_2 \). If there be no common
subgraph $G'$ that has more nodes than $G$, and $G$ will be the maximum subgraph of $G_1$ and $G_2$ denoted by $G=mcs(G_1, G_2)$. The similarity of $G_1$ and $G_2$ is shown in formula (2).

$$\text{sim}(G_1, G_2) = \frac{|mcs(G_1, G_2)|}{\max(|G_1|, |G_2|)}$$

where $|G| = |v| + |e|$

<table>
<thead>
<tr>
<th>Program Name</th>
<th>Original program size (kb)</th>
<th>Program size of one time iteration (kb)</th>
<th>Program size of two times iteration (kb)</th>
<th>Program size of three times iteration (kb)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ShellSort.exe</td>
<td>29</td>
<td>31.5</td>
<td>32.5</td>
<td>34</td>
</tr>
<tr>
<td>InsertSort.exe</td>
<td>29</td>
<td>31.5</td>
<td>32.5</td>
<td>38.5</td>
</tr>
<tr>
<td>BubbleSort.exe</td>
<td>29</td>
<td>31.5</td>
<td>33</td>
<td>42.5</td>
</tr>
<tr>
<td>QuickSort.exe</td>
<td>29</td>
<td>32</td>
<td>33.5</td>
<td>42.5</td>
</tr>
</tbody>
</table>

<table>
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<tr>
<th>Program Name</th>
<th>Original execution time (ms)</th>
<th>Program execution time of one time iteration (ms)</th>
<th>Program execution time of two times iteration (ms)</th>
<th>Program execution time of three times iteration (ms)</th>
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</thead>
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<tr>
<td>ShellSort.exe</td>
<td>30.7</td>
<td>320.6</td>
<td>495.9</td>
<td>633.6</td>
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<tr>
<td>InsertSort.exe</td>
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<td>63763.1</td>
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<td>339284.4</td>
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<td>BubbleSort.exe</td>
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<td>55820.5</td>
<td>58027.2</td>
<td>60454.5</td>
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<td>39.3</td>
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<table>
<thead>
<tr>
<th>Program Name</th>
<th>File Size (kb)</th>
<th>Execution Time (ms)</th>
<th>Number of Basic Block</th>
<th>Number of Edge</th>
<th>Number of Instruction</th>
</tr>
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<tbody>
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<td>310.7</td>
<td>21</td>
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<tr>
<td>Shell2_deo</td>
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<td>Shell3_deo</td>
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V. EXPERIMENT AND ANALYSIS

Based on the environment that is Intel(R) Core(TM)2 CPU 1.86GHz, Microsoft Windows XP Professional 5.1.2600 Service Pack 3, Visual Studio 2008, with the obfuscation steps, obfuscation based on instruction fragment diversification and control flow randomization is implemented, programs of Shell Sorting, Insertion Sorting, Bubble Sorting and Quick Sorting are obfuscated, the source code of four programs are shown in Appendix A, static reverse analysis tool of IDA\(^1\) is used to analyze the obfuscated programs, and the performance of original and obfuscated programs are compared.

A. Cost Validation

Four debug version programs are built, and their control flow graphs are constructed by static disassembly. They are obfuscated by the followed method. Firstly, instruction fragment will be divided into two parts that will generate three kinds of instruction fragments respectively with the instruction equivalent transformation rules, and the forms of these fragments are different, but they are semantics-preserving. Random function is used to choose these instruction fragments. When the iteration times are 1, 2 and 3 respectively, a set of obfuscated programs are constructed, the size of original and obfuscated programs are shown in Table V. As the iteration times increased, the file size of programs will be increased. To reduce other factors that may affect program execution, the average execution

time of 10 times are measured. Sorting programs are used to sort 50000 numbers generated by random function, the execution time of original and obfuscated programs are shown in Table VI. Due to the various sorting algorithms, execution time of different sorting programs is different. As the iteration time increased, and the number of random function and instruction is increased, it will enhance the program time overhead.

B. Potency Validation

Benchmark programs are obfuscated by obfuscation as iteration time being 1, 2 and 3. With the IDA plus, the number of basic blocks, edges and instructions can be gained that are shown in Fig. 4-Fig. 6. The numbers of basic blocks in four original programs are shown in Fig. 4, they are 14, 10, 11 and 11, and they will be exponentially increased with instruction fragments diversification, as iteration time be 3, the numbers of basic blocks are 113, 363, 368 and 514. The numbers of control flow edges in four original programs are shown in Fig. 5, they are 17, 12, 13 and 15. With multi-way branches and random functions, control flow paths are increased greatly, when iteration time is 3, they are 158, 376, 532 and 751. The numbers of instructions are shown in Fig. 6, the total numbers of instructions of programs are 74, 54, 56 and 71. With equivalent instruction expansion rules, diversified instruction fragments will be generated, and the numbers of instructions will be increased. With one time iteration, the numbers of instructions are 157, 119, 124 and 221. While the iteration time is three, the numbers of instructions in four programs are 860, 1949, 2869 and 3230. After many times iteration, it will be more difficult for attackers to reverse analyze obfuscated programs.

\(^1\) IDA Multi-Processor Disassembler and Debugger. http://www.hex-rays.com
Obfuscated programs of Shell Sorting, Insertion Sorting, Quick Sorting, and Bubble Sorting are constructed, and their control flow graphs are generated by IDA, all of them are shown in Fig. 7-Fig. 10. The original control flow graphs of four sort programs are simple, as the iteration time increasing, their control flow graphs will be complicated, and the difficulty of static and dynamic reverse analysis will be increased.

Execution traces of four sorting programs can be obtained by dynamic tracking. With the same input for many times, the execution traces of program are same. Through iteration obfuscation, control flow structure of sorting programs will be complicated, and the execution paths will be diversified. Each execution path will be different by affecting instruction fragment diversification and control flow randomization, and iteration obfuscation can resist dynamic reverse analysis to a certain extent.

C. Deobfuscation Validation

Open source tool optimice\(^2\) is used to attack obfuscated programs, and it can validate the deobfuscation performance of obfuscation. The tool implements control flow reduction, junk instruction removing, constant propagation and folding. When diversified instruction fragments are generated with junk instruction insertion, deobfuscation tool can effectively remove the invalid instructions in multi-way branches, but cannot normalize the instruction fragments in different branches. Thus the number of instructions in basic block will be reduced, but the control flow graph will not be changed. Four sort programs are deobfuscated by the tool, the detail information are shown in Table VII. The numbers of basic blocks and edges in obfuscated programs will not be changed by deobfuscation, their control flow graph will not be changed.

Due to the deobfuscation algorithm, the size and instruction

number of programs are decreased to some extent, thus which decreases the program execution times, and it can be validated that iteration obfuscation have ability to resist the deobfuscation attacks.

![Fig. 9. Control flow graph of original and obfuscated (1-3 times iteration) quick sorting program.](image)

![Fig. 10. Control flow graph of original and obfuscated (1-3 times iteration) bubble sorting program.](image)

D. Stealth Validation

Stealth can be determined by the similarity of original and obfuscated programs. Similarity of original and 3rd time iteratively obfuscated programs are calculated, the values are 8.32%, 12.45%, 7.92%, 8.35% and 7.02%. It reflects that iteration obfuscation greatly increases the number of control flow nodes and edges. With iteration obfuscation, the structure of control flow graph is changed, and the stealth is low on the whole, it is easy for attackers to determine whether program is obfuscated or not.

VI. CONCLUSION AND FUTURE WORKS

Obfuscation based on instruction fragment diversification and control flow randomization is proposed, the rules of instruction expansion, equivalent instruction replacement, register transformation, junk instruction insertion and instruction position exchanging are used to construct diversified fragments, and multi-way branches are constructed. Instruction fragments are used to be the execution sequences, random functions are used to choose the multi-way branches. By iteration obfuscation, the structure of control flow graph will be complicated, nodes and edges of control flow graph will be greatly increased, and the difficulty of static reverse is increased. Multi-way branches are constructed, instruction trace will be different as the same input, and the difficulty of dynamic reverse is enhanced. In the future work, idea of diversification and randomization will apply to other obfuscation such data obfuscation and layout obfuscation to improve the potency and stealth of the obfuscation.

APPENDIX A

ShellSort.exe

```c
void ShellSort(int v[],int n){
    int gap,i,j,temp;
    for(gap=n/2;gap>0;gap /= 2){
        for(i=gap;i<n;i++){
            for(j=i-gap;(j >= 0) && (v[j] > v[j+gap]);j -= gap ){
                temp=v[j];
                v[j]=v[j+gap];
                v[j+gap]=temp;
            }
        }
    }
}
```

InsertionSort.exe

```c
void InsertionSort(int input[],int len){
    int i,j,temp;
    for (i = 1; i < len; i++){
        temp = input[i];
        for (j = i - 1;j>-1&&input[j] > temp ; j--){
            input[j + 1] = input[j];
            input[j] = temp;
        }
    }
}
```

BubbleSort.exe

```c
void BubleSort(int a[],int n){
    int i,j,k;
    for(j=0;j<n;j++){
        for(i=0;i<n-j;i++){
            if(a[i]>a[i+1]){ k=a[i];
                a[i]=a[i+1];
                a[i+1]=k;
            }
        }
    }
}
```
```c
void QuickSort(int data[], int low, int high)
{
    int mid;
    int ran;
    data[0]=data[low];
    mid=data[low];
    while((low < high) && (data[high] >= mid)) {
        --high;
    }
    data[low]=data[high];
    while((low < high) && (data[low] < mid) { +low;
    }
    data[high]=data[low];
    data[low]=data[high];
    return low;
}

int Partition(int data[], int low, int high)
{
    int mid;
    int ran;
    data[0]=data[low];
    mid=data[low];
    while((low < high) && (data[high] >= mid)) {
        --high;
    }
    data[low]=data[high];
    while((low < high) && (data[low] < mid) { +low;
    }
    data[high]=data[low];
    data[low]=data[high];
    return low;
}

REFERENCES


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311
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